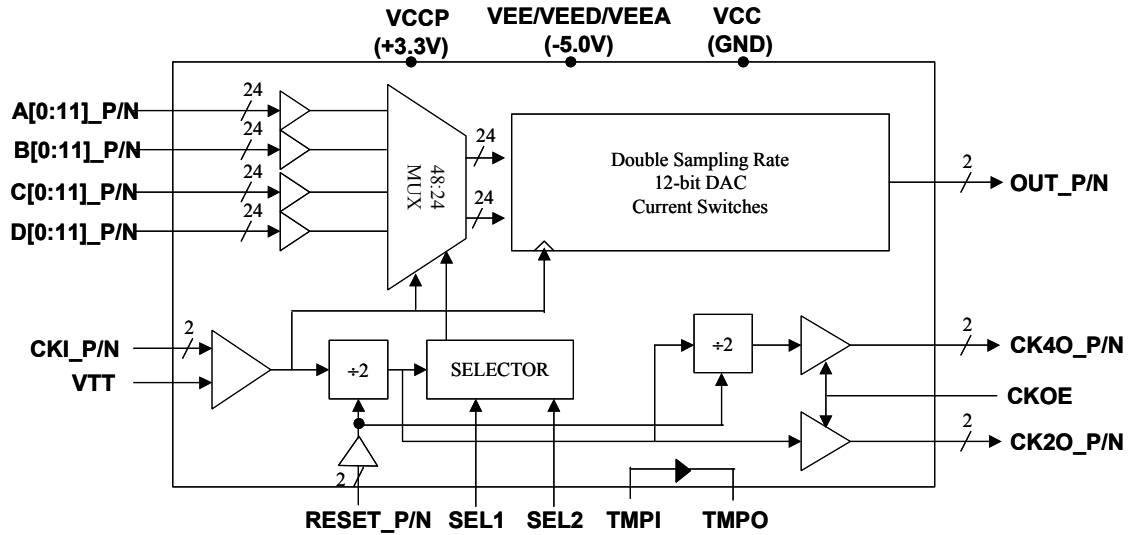


## MD662H – Double Sampling Rate > 8 Gbps MUXDAC



### Key Features

- 4:1 multiplexing ratio for each input bit of DAC
- Double Sampling Rate (DSR) DAC enables analog output sampling rate twice of the clock rate
- Clock rate up to > 4 GHz with DAC analog output sampling rate > 8 Gbps
- Complementary outputs with 50-Ω back terminations
- Complementary divided-by-2 and divide-by-4 clock LVDS outputs for data synchronization
- Divide-by-2 and Divide-by-4 clock output drivers with enable/disable control without interrupting internal operations of the chip
- On-chip 100 ohm termination between each differential LVDS input data and RESET pair
- 3.5W power consumption
- TQFP package with Exposed Pad to enhance grounding and heat dissipation

### Applications

- Arbitrary waveform generation
- Radar/Ladar design and testing
- Software defined radio
- Electronic warfare
- Wireless basestations
- RF signal source generation
- Hard disk and magnetic storage testing
- WLAN testing
- Advanced communication modulations

### Description

The **MD662H** is a high-speed 12-bit Digital to Analog Converter (DAC) integrated with 4:1 multiplexer for each DAC input bit. The digital data inputs are LVDS with on-chip 100 ohm termination resistors. The Double Sampling Rate (DSR) DAC enables its analog output sampling rate twice of the clock rate. The device can be clocked up to > 4 GHz to achieve > 8 Gbps at DAC outputs. Complementary outputs are available with 50-Ω output back terminations. Divided-by-2 and Divide-by-4 clock LVDS outputs (**CK20\_P/N** & **CK40\_P/N**) and sampling phase selection (**SEL1**) are provided to ease the alignment of sampling phase relative to the input data. **CKOE** pin is provided to enable/disable output drivers of **CK20\_P/N** and **CK40\_P/N** clock outputs without interrupting the internal operations for the convenience of system applications.

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